





15. The drive circuit as specified in Claim 14 wherein the voltage mode feedback includes a capacitor coupled at the drive amplifier output, wherein the voltage mode feedback senses the voltage at the capacitor.

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16. The drive circuit as specified in Claim 15 wherein the voltage mode feedback has an adjustable gain being variable as a function of the number of piezo actuators driven.

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17. The drive circuit as specified in Claim 16 wherein the adjustable gain is accomplished using a variable resistor in the voltage mode feedback.

18. The drive circuit as specified in Claim 1 wherein the drive amplifier has a first output, and a second output having a current mirror based on the first output.

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19. The drive circuit as specified in Claim 18 wherein a capacitor is coupled to the first output and the piezo actuators are adapted to be driven by the second output.

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20. The drive circuit as specified in Claim 19 wherein a first time constant formed by the capacitor and the voltage mode feedback, and a second time constant formed by the piezo actuators and the voltage mode feedback, are substantially equal.

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21. The drive circuit as specified in Claim 14 further comprising a DC control circuit controlling the DC value at the piezo actuator.

22. The drive circuit as specified in Claim 21 wherein the DC control circuit is integrated into the low frequency compensation loop.

23. The drive circuit as specified in Claim 1 further comprising a digital-to-analog (DAC) coupled to one drive amplifier input and a voltage reference being coupled to another drive amplifier input.

24. The drive circuit as specified in Claim 1 further comprising an ADC coupled to the calibration circuit.